



MALLA REDDY COLLEGE OF ENGINEERING & TECHNOLOGY

(Autonomous Institution-UGC, Govt of India)

DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

TIME TABLE A.Y. 2024-2025

III EEE II SEMESTER

W.E.F.9thDEC 2024

DAY/HOUR	9:20AM-10:20AM	10:20AM-11:20AM	11:20AM-11:30AM	11:30AM-12:30PM	12:30PM-1:30PM	1:30PM-2:30PM	2:30PM-3:30PM	
MONDAY	DSP	PSD	BREAK	PSP	L U N C H	MPMC	DBS	
TUESDAY	PSD	DBS		MPMC		PSP	DSP	
WEDNESDAY	PSP	PSD		DSP		DSP LAB		
THURSDAY	DBS	PS LAB		MPMC		DSP/PSP(T)		
FRIDAY	MPMC	PSD		BREAK		DBS	AD-II	
SATURDAY	PSP	MPMC LAB				DSP	DBS/MPMC(T)	

Note : Tutorial Class-Weekly one Subject

Name of the Subject	Name of the faculty	Contact No.
Power System Protection(PSP)	Mr.K.SRAVAN KUMAR	9182187511
Power Semiconductor Drives(PSD)	Dr. M. SHARANYA	9849248291
Digital Signal Processing(DSP)	Mrs.S REVATHI	7661028541
Microprocessors & Microcontrollers(MPMC)	Mr. R SATHISH KUMAR	9491050418
Data Base Systems(DBS)	Mr.G.NARESH	9063924923
Digital Signal Processing Laboratory(DSP Lab)	Mrs.S REVATHI/	7661028541
Power System Laboratory(PS Lab)	Mr. R SAI KIRAN/ Mr.G.NARESH/ Mr.SUNDARAI AH N	8500559180 9063924923 9390659596
Microprocessors & Microcontrollers Laboratory(MPMC Lab)	Mr. R SATHISH KUMAR / Mrs.K VIJAYA BHARATHI	9491050418 9292103014
Application Development-II (AD-II)	Mr.M KUMARA SWAMY/ Mr. K ARAVINDA SWAMY	9542690725 7382715484

ROOM NO: 6212

Class In charge	Mr. K ARAVINDA SWAMY	7382715484
Mentors	Name of the Faculty	R. No.
	Mr. K ARAVINDA SWAMY	Reg201--220
	Mr.M KUMARA SWAMY	Reg 211- LE 208

COORDINATOR
Mr. M. Naresh

HOD,EEE
Dr.M.Sharanya